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Amendments to the claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of claims:

1. (currently amended) A method Method for the uniform output of
asynchronously transmitted digital values, comprising:-(D) with an output clock (fA, fB) in a
receiver (A, B), characterized in that the receiver (A, B) determines the amount of digital values
(D) received from the receiver (A, B) in relation to the time and dependent on this sets the output
clock (fA, fB) in such a way that the digital value (D) is outputted at the frequency, with which
on average the receiver (A, B) receives the digital values (D)

receiving the digital values in a receiver from a transmission path;
outputting the digital values from the receiver on the basis of an output clock for further
processing;

transmitting the digital values to the transmission path by a transmission device of the
receiver;

determining the amount of the digital values received by the receiver in relation to the
time;

adjusting the output clock on the basis of the determined amount in such a way that the
digital values are outputted at a frequency with which on time average the receiver receives the
digital values; and

adjusting a transmission clock of the transmission device to correspond to the output
clock of the receiver.

2. (currently amended) The method Method according to claim 1, wherein the
characterized in that digital values (D), which were destined for the receiver (A, B), but have not
reached the receiver (A, B) are considered as received digital values when determining the
amount of the digital values (D) received from the receiver (A, B) as received digital values (D).

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3. (currently amended) The method Method according to claim 2, wherein
characterized in that the information about the amount of digital values (D) received from the
receiver (A, B) (D) or of the digital values (D) destined for the receiver (A, B) are interpolated is
extracted from the information data packets[,] which are produced by the transmitter
transmission device (A, B), which sends sending out the digital values (D) destined for the
receiver (A, B).

4. (currently amended) The method Method according to claim 1, wherein
characterized in that the output clock is derived from an output signal of an oscillator (OSC),
which supplies, supplying without any adjusting operation, a nominal frequency[,] which is
could be influenced by means of an adjusting operation.

5. (currently amended) The method Method according to claim 4, wherein
characterized in that the output signal of the oscillator (OSC) is the operating clock for the
receiver (A, B).

6. (currently amended) The method Method according to claim 4, wherein
characterized in that the output clock is produced by dividing the output signal of the oscillator
(OSC).

7. (canceled)

8. (currently amended) The method Method according to claim 1-claim 7, wherein
characterized in that the digital values (D) are transmitted bi-directionally between a first
receiver (A) and a second receiver (B) and both of the receivers (A, B) set adjust their output
clock for received digital values (D) dependent on the amount of digital values (D) in relation to
the time, which in each case have been sent out by the other receiver (A, B).

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9. (currently amended) The method Method according to claim 8, wherein characterized in that both of the receivers (A, B) set adjust both the output clock for received digital values (D) and the transmission clock for sent digital values (D).

10. (currently amended) The method Method according to claim 1, wherein characterized in that the digital values (D) are outputted in analog form.

11. (currently amended) The method Method according to claim 1, wherein characterized in that the digital values (D) are speech signals, which are transmitted in a system for providing a telephone service via a communication network (IP-Net).

12. (currently amended) The method Method according to claim 1, wherein characterized in that the receiver (A, B) receives the digital values (D) from a self-clocked data decoder or data encoder (CODEC).

13. (currently amended) A device-Device for the uniform output of asynchronously transmitted digital values, comprising (D) with an output clock (fA, fB), characterized in that the device (VOIP) has a clock generation unit (CGU), which is set up in such a way that it can determine the amount of digital values (D) received from the device (VOIP) in relation to the time and dependent on this, can set the output clock (fA, fB) in such a way that the digital values (D) are outputted at the frequency, with which over the average time digital values (D) are received from the device (VOIP).

a receiver to receive the digital values from a transmission path and to output the digital values on the basis of an output clock for further processing;

a transmitter to transmit the digital values to the transmission path; and
a clock generation unit to determine the amount of digital values received by the device in relation to the time,

wherein the clock generation unit is configured to adjust the output clock dependent on the determined amount in such a way that the digital values are outputted at the frequency, with which on time average digital values are received by the receiver, and

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wherein a transmission clock of the transmitter corresponds to the output clock of the receiver.

14. (cancelcd)

15. (new) A bi-directional data transmission system, comprising:
a first receiver, and
a second receiver coupled to the first receiver via a transmission path to receive
asynchronously transmitted digital values from the transmission path and to output the digital
values on the basis of an output clock for further processing, wherein the first receiver and the
second receiver each comprise:
a transmitting device to transmit digital values to the transmission path on the basis of a
transmission clock corresponding to the output clock of the receiver; and
a clock generation unit to generate the output clock of the receiver,
wherein for at least one of the first receiver and the second receiver, the clock generation
unit is configured to determine an amount of the digital values received by the at least one of the
first receiver and the second receiver in relation to time and to adjust the output clock dependent
on the determined amount such that the digital values are outputted at a frequency with which on
time average the digital values are received by the at least one of the first receiver and the second
receiver.